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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	Based on 09/714,891-10/645516
Sheet	1	of	3	Filing Date	November 17, 2000 8/22/03
				First Named Inventor	Koji ONO et al.
				Group Art Unit	2815
				Examiner Name	Jerome Jackson Jr.
				Attorney Docket Number	0756-7186

U.S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ²	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Number	Kind Code ³ (if known)		
<i>P</i>		5,643,826		Ohtani et al.	07/01/97
<i>P</i>		5,923,962		Ohtani et al.	07/13/99
<i>P</i>		5,247,190		Friend et al.	09/21/93
<i>P</i>		5,399,502		Friend et al.	03/21/95

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FOREIGN PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ²	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY
		Office ⁴	Number ⁴ (if known)		
<i>P</i>		JP	07-130652		05/19/95
<i>P</i>		JP	08-078329		03/22/96
<i>P</i>		JP	10-135468		05/22/98
<i>P</i>		JP	10-135469		05/22/98
<i>P</i>		JP	10-294280		11/04/98
<i>P</i>		JP	11-191628		07/13/99
<i>P</i>		JP	10-092576		04/10/98
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<i>P</i>		JP	11-354442		12/24/99
<i>P</i>		pet	WO90/13148		11/01/90
					Eng.

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ²	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ³
<i>P</i>		Shimokawa et al., "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, Vol. 27, No. 5, May, pp. 751-758, 1988.			
<i>P</i>		Hatano et al., "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT With High Reliability and Performance," IEDM Technical Digest 97, pp. 523-526.			
<i>P</i>		Hermann Schenk et al., "Polymers for Light Emitting Diodes," EuroDisplay '99 Proceedings, pp. 33-37.			

Examiner Signature	<i>[Signature]</i>	Date Considered	2/19/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

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10/21/03

U.S. PATENT DOCUMENTS

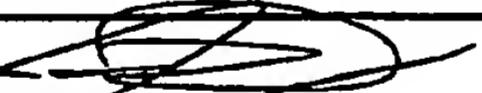
Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Person or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
1		4,394,182		Maddox III	07/19/83	
2		5,528,397		Zavracky et al.	06/18/96	
2		5,583,369		Yamazaki et al.	12/10/96	
2		5,686,328		Zhang et al.	11/11/97	
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2		5,736,750		Yamazaki et al.	04/07/98	
2		5,830,787		Kim	11/03/98	
2		5,949,107		Zhang	09/07/99	
2		6,001,714		Nakajima et al.	12/14/99	
2		6,030,667		Nakagawa et al.	02/29/00	

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		Office ⁴	Number ⁴				
2		JP	09-191111		07/22/97		Abst.
2		JP	09-293600		11/11/97		Abst.
2		JP	10-104659		04/24/98		Abst.
2		JP	10-233511		09/02/98		Abst.
2		JP	04-369271		12/22/92		Full

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					T ³
2		Specifications and Drawings for Application Serial No. 09/619,732, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 19, 2000.					
2		Specifications and Drawings for Application Serial No. 09/618,930, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 18, 2000.					
2		Specifications and Drawings for Application Serial No. 09/433,705, "Method of Fabricating a Semiconductor Device," Filed on November 4, 1999.					

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2	5,323,042			Matsumoto	06/21/94
2	6,198,133	B1		Yamazaki et al.	03/06/01
2	6,281,552	B1		Kawasaki et al.	08/28/01
2	6,166,414			Miyazaki et al.	12/26/00
2	5,393,682			Liu	02/28/95
2	5,100,820			Tsubone	03/31/92
2	6,534,826			Yamazaki	03/18/03

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2	JP	05-102483			04/23/93
2	JP	8-274336			10/18/96
2	JP	7-235680			09/05/95
2	JP	6-148685			05/27/94
2	EP	1 003 223		Yamazaki	05/24/00
2	EP	1 049 167		Yamazaki	11/02/00

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